



IRIS-Base - Pinout



Pin number Function

- 1 Gnd
- 2 N/A (I2C SDA)*
- 3 N/A (I2C SCL)*
- 4 Gnd
- 5 N/A (GPIO 2)*
- 6 TXD (from uP) (connect to RXD)*
- 7 RXD (from uP) (connect to TXD)*
- 8 Sleep activation (GPIO 1)*
- 9 Reset (active low, only uP)*
- 10 N/A (SPI SCK)*
- 11 N/A (SPI MISO)*
- 12 N/A (SPI MOSI)*
- 13 N/A (SPI SS)*
- 14 Power +5 Volt**
- 15 Power +5 Volt**
- 16 Analogue/Digital in 6
- 17 Analogue/Digital in 5
- 18 Analogue/Digital in 4
- 19 Analogue/Digital in 3
- 20 Analogue/Digital in 2
- 21 Analogue/Digital in 1
- 22 Gnd
- 23 Gnd
- 24 Digital out 4
- 25 Digital out 3
- 26 Digital out 2
- 27 Digital out 1
- 28 Gnd

* Used only for special applications and should normally not be connected. ** Must be regulated and well filtrated against high frequent noise.

All pins are connected to the microprocessor (Atmel Mega32). See datasheet for specifications and limitations regarding voltage levels. http://www.atmel.com/dyn/resources/prod_documents/doc2503.pdf









Antenna

50 ohm



Connections

2 x 14 pin header (2.54mm pitch)

For more information: www.irisnetwork.se, info@irisnetwork.se

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